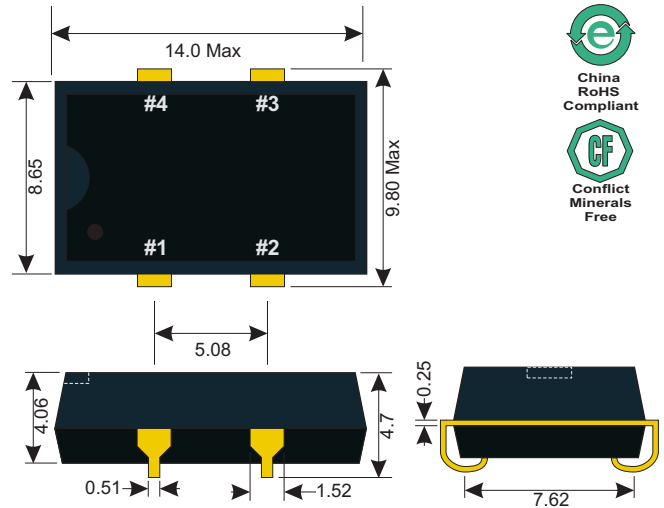




Specifications

Package	POM2R - Plastic J-Lead	
Frequency Range (fo)	1.500MHz ~ 75.000MHz	
Logic Family	HCMOS	
Freq. Stability (Df/fo)	±100ppm (std.)	
Aging/Year (fa)	5ppm	
Temp. Range	Operating (TOPR)	-20°C ~ +70°C
	Storage (TSTG)	-40°C ~ +125°C

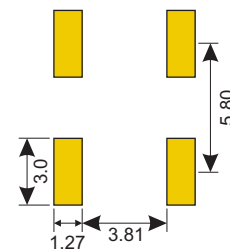
Dimensions (mm)



Electrical - HCMOS

Power Supply	Voltage (VDD ±10%)	5.0VDC
	Current (ICC) 1.5 ~ 20MHz 20.0 ~ 75MHz	40mA 65mA
	Standby Current	10µA Max
Output	Load	15pF
	Voltage (VOL)	10% Vdd
	Voltage (VOH)	90% Vdd
Rise (TR) & Fall (TF) Time 1.5 ~ 20MHz 20.0 ~ 75MHz		8nS Max 5nS Max
Symmetry/Duty		40/60

Recommended Pad (mm)



Tri-State

U Option (Page 2)	Default	U2
70%Vcc Min (Pin 1)	Enabled Active (Pin 3)	Disabled High Z (Pin 3)
N/C or Open (Pin 1)	Enabled Active (Pin 3)	Disabled High Z (Pin 3)
30%Vcc Max (Pin 1)	Disabled High Z (Pin 3)	Enabled Active (Pin 3)
Enable Time	200µS Max	200µS Max
Disable Time	50µS Max	50µS Max

Absolute Maximum Ratings

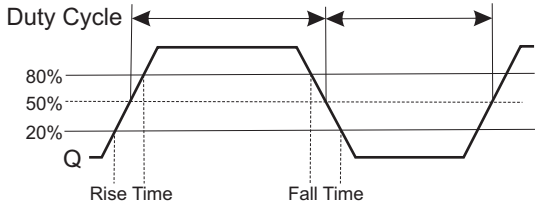
Maximum Storage Temp	-55°C to +85°C
Voltage (VDD)	6.5 VDC
Input Voltage	-0.5Vdc ~ Vdd+0.5Vdc

Connections

Pin 1	Tri-State
Pin 2	Ground
Pin 3	Output N
Pin 4	Vdd



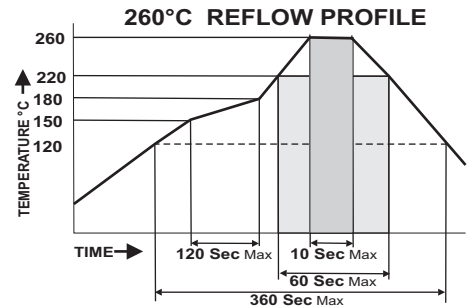
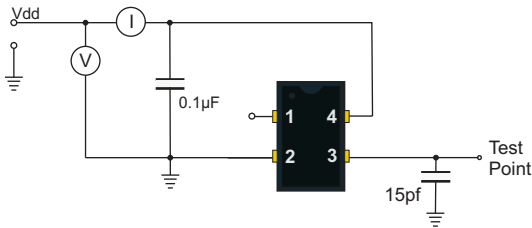
Waveform



Environmental And Mechanical

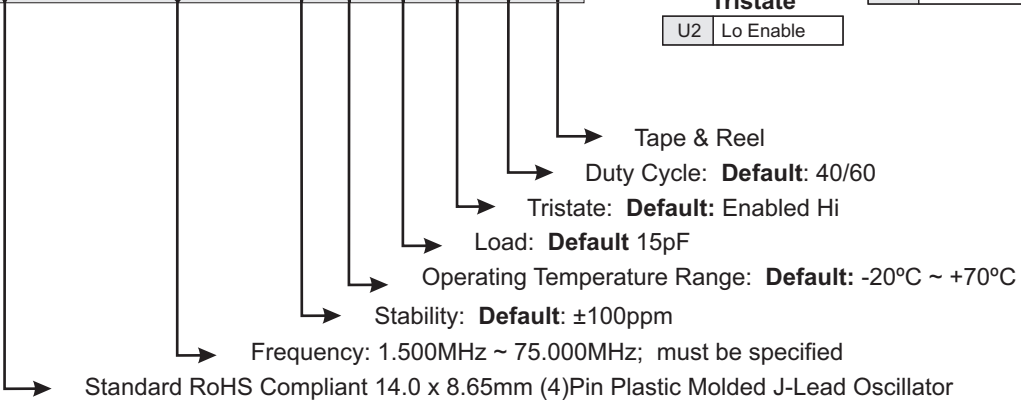
Temperature	10 Cycles of -30°C (30Mins), Normal (1Hr), 85°C (30Mins), Normal (1Hr)
Shock	Accelerated at 1000G for 1mS in each perpendicular axis.
Vibration	4 Cycles of 20G acceleration at 20 - 2,000Hz within 4 Minutes in each perpendicular axis.
Solder	Peak Temperature of 260°C Max for 10 Seconds with preheat of 160°C for 90±10% for 10 Seconds for a Maximum of 2 Cycles.

HCMOS Test Circuit



Part Number

POM2R - XXX.XXX M-SX RX WX UX QX-T



OP Temp	R6 -40 ~ +85°C	Stability	S1 ±50ppm SC Cust. Stability	Duty Cycle	Q1 45/55 Duty
Tristate	U2 Lo Enable	Load	W2 50pF WC Cust. Load		

Example: **POM2R-70.000M-T** 70.000MHz, 5.0V ±100ppm Oscillator operating at -20°C ~ +70°C, Delivered on Tape & Reel
 Example: **POM2R-70.000M-S1R6-T** 70.000MHz, 5.0V ±50ppm Oscillator operating at -40°C ~ +85°C, Delivered on Tape & Reel